



YJN280G10H

N-Channel Enhancement Mode Field Effect Transistor

Product Summary

V_{DS}	100V
I_D	280A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	2.6m
$R_{DS(ON)}$ (at $V_{GS}=6V$)	3.0m
100% EAS Tested	
100% V_{DS} Tested	

General Description

Split Gate Trench MOSFET technology



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Electrical Characteristics ($T_J=25$ unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
Static Parameter						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$				



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Typical Electrical and Thermal Characteristics Diagrams

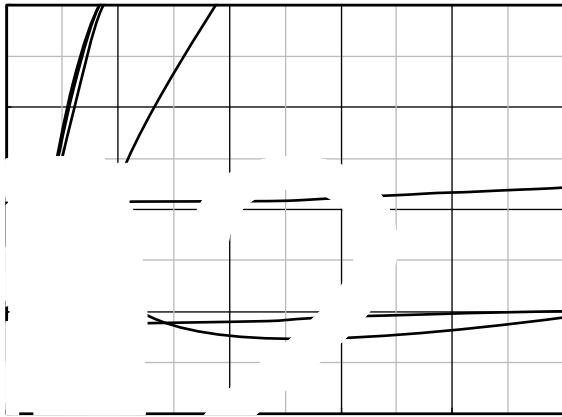


Figure 1. Output Characteristics

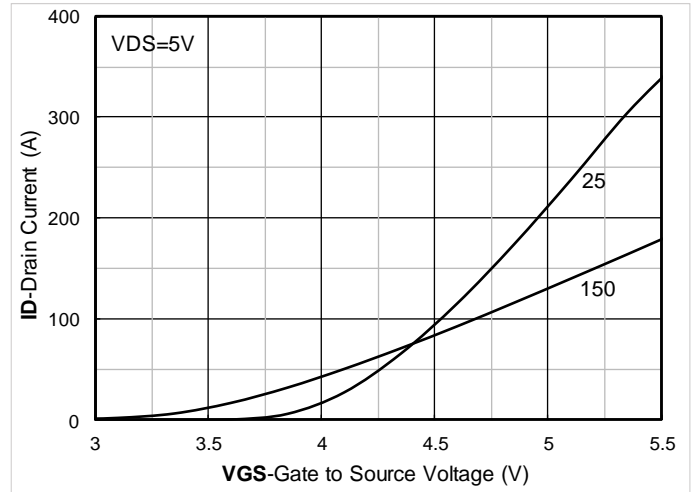


Figure 2. Transfer Characteristics

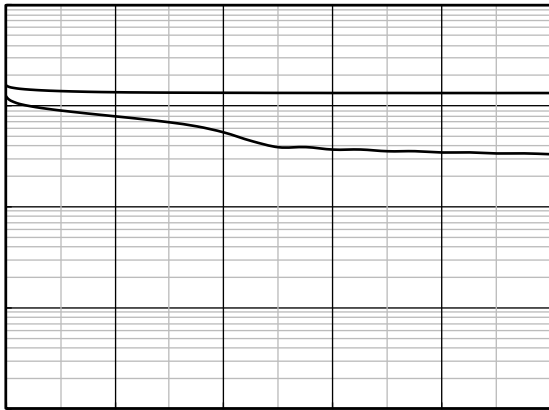


Figure 3. Capacitance Characteristics

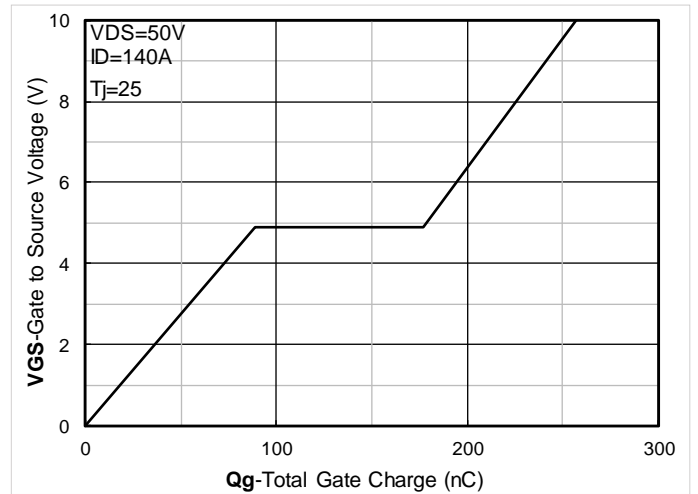


Figure 4. Gate Charge

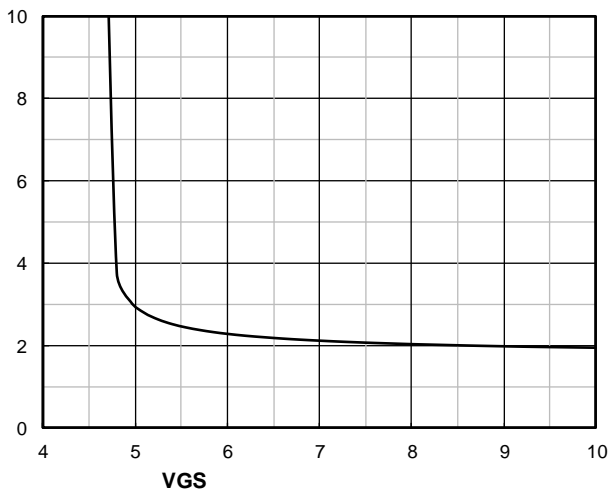


Figure 5. On-Resistance vs Gate to Source Voltage

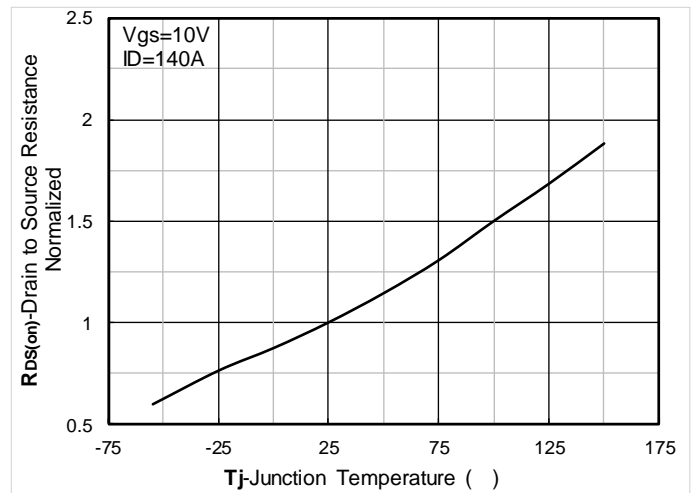


Figure 6. Normalized On-Resistance

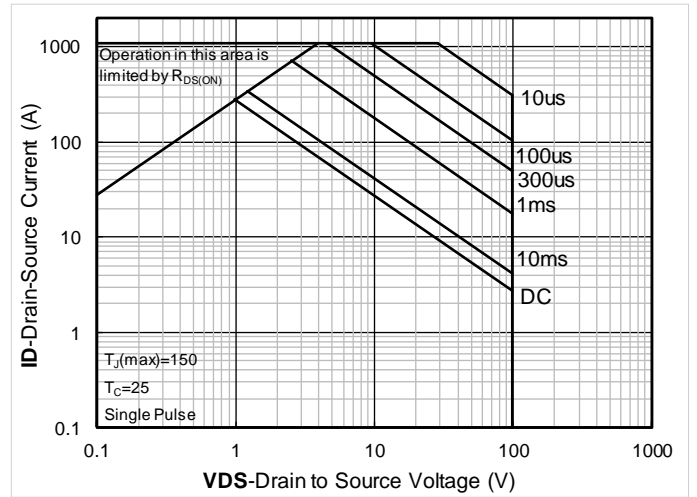


Figure 13. Maximum Transient Thermal Impedance

Figure 14. Safe Operation Area

Test Circuits & Waveforms

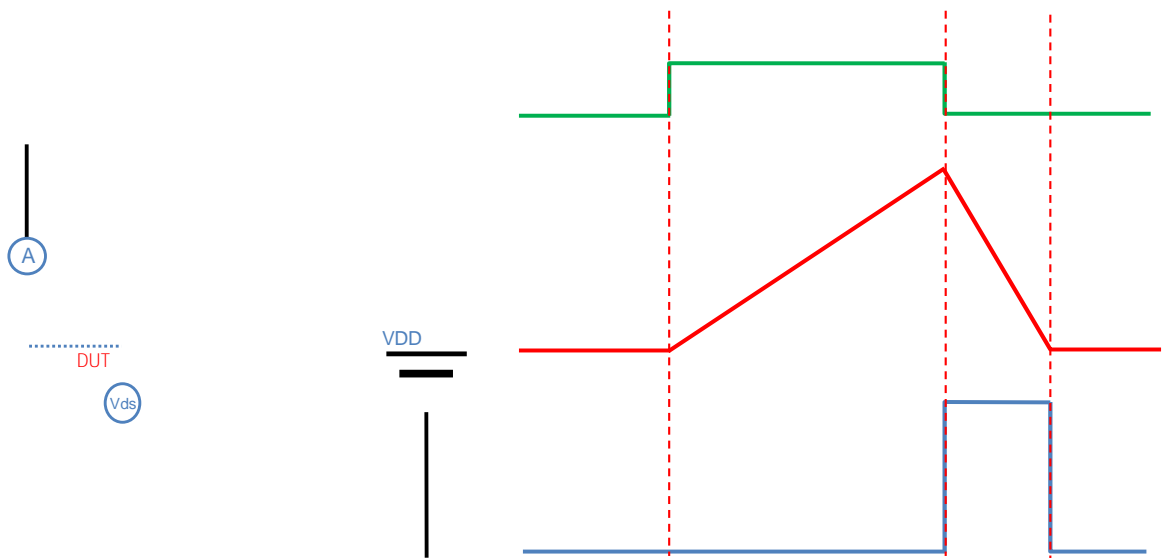


Figure A. Unclamped Inductive Switching (UIS) Test Circuit & Waveform



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Figure B. Gate Charge Test Circuit & Waveform



Figure C. Resistive Switching Test Circuit & Waveform

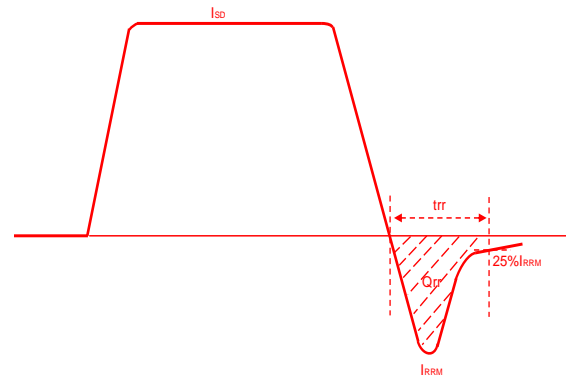
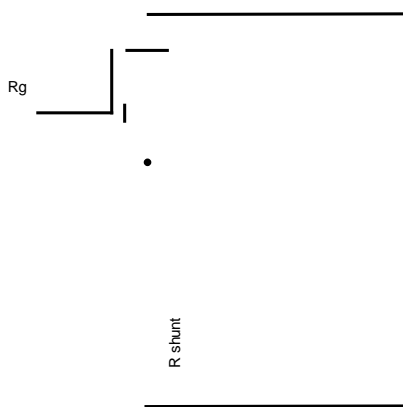


Figure D. Diode Recovery Test Circuit & Waveform



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TO-247AB Package information

O-247AB		
Dim	Min	Max
A	4.80	5.20



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Disclaimer

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