



## Dual PNP Digital Transistors (Built-in Resistors)

### Features

- " Epoxy meets UL-94 V-0 flammability rating
- " Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- " Surface mount package ideally Suited for Automatic Insertion

### Mechanical Data

- " Package: SOT-363
- " Terminals:



## UMB1N

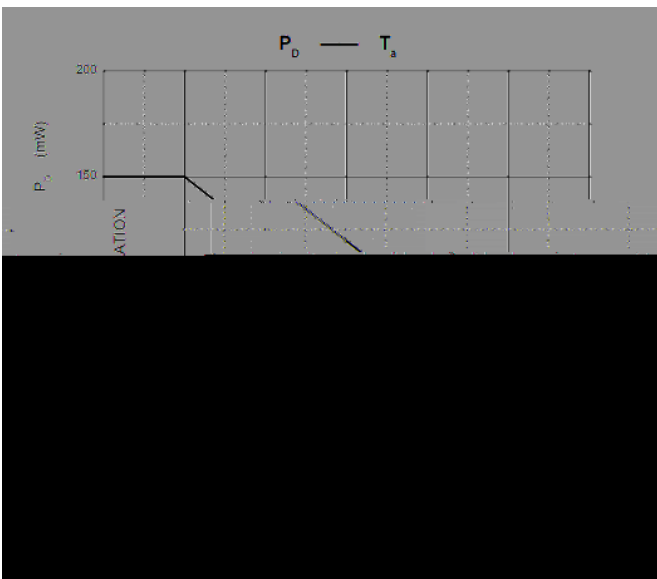
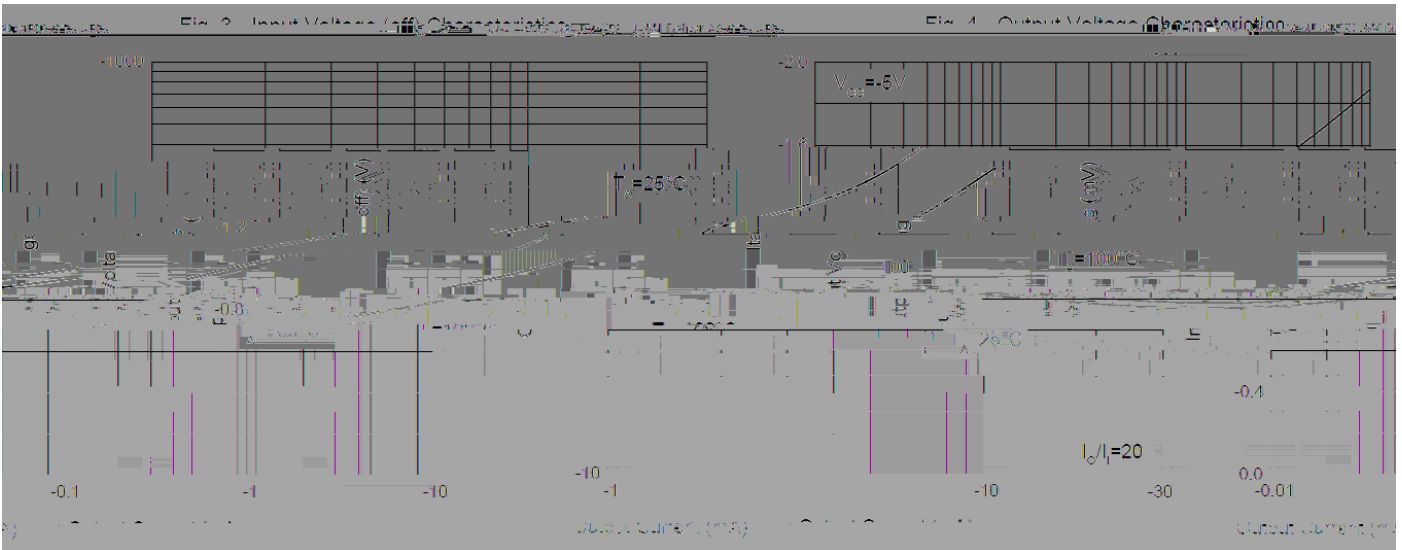
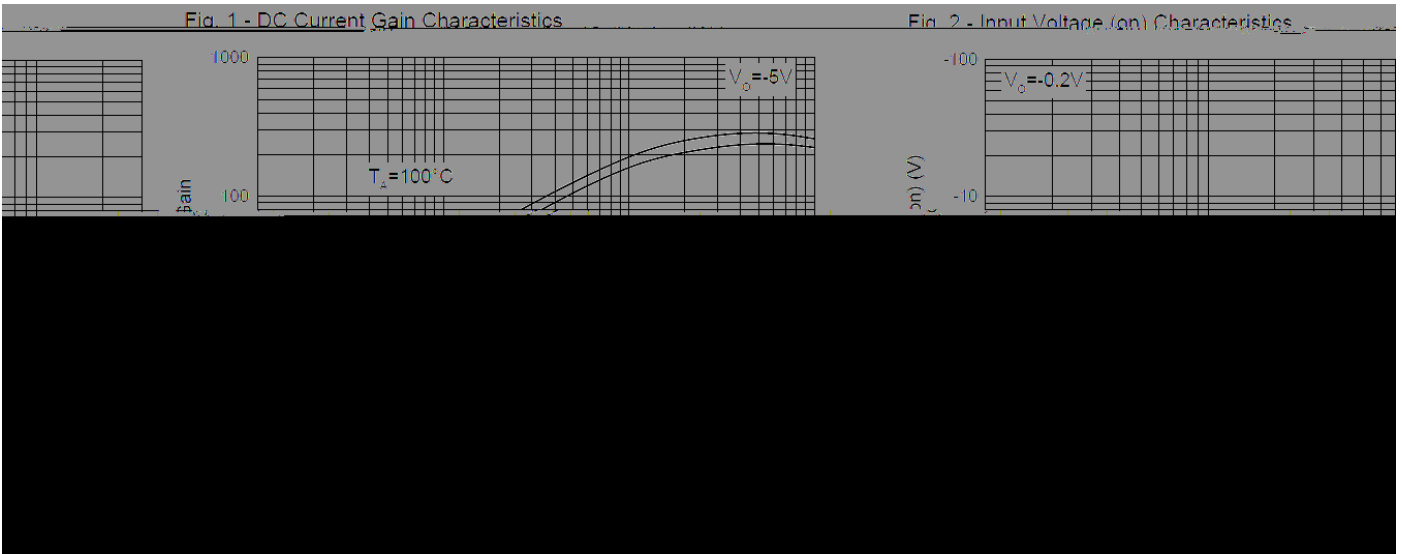
Maximum Ratings (Ta=25 Unless otherwise specified Å

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V <sub>CC</sub>	V		-50
Input Voltage	V <sub>IN</sub>	V		-40 to +10
Output Current	I <sub>o</sub>	mA		-100
Power Dissipation	P <sub>D</sub>	mW		150
Junction Temperature	T <sub>j</sub>			150
Storage Temperature	T <sub>STG</sub>			-55 to +150

Electrical Characteristics (Ta=25

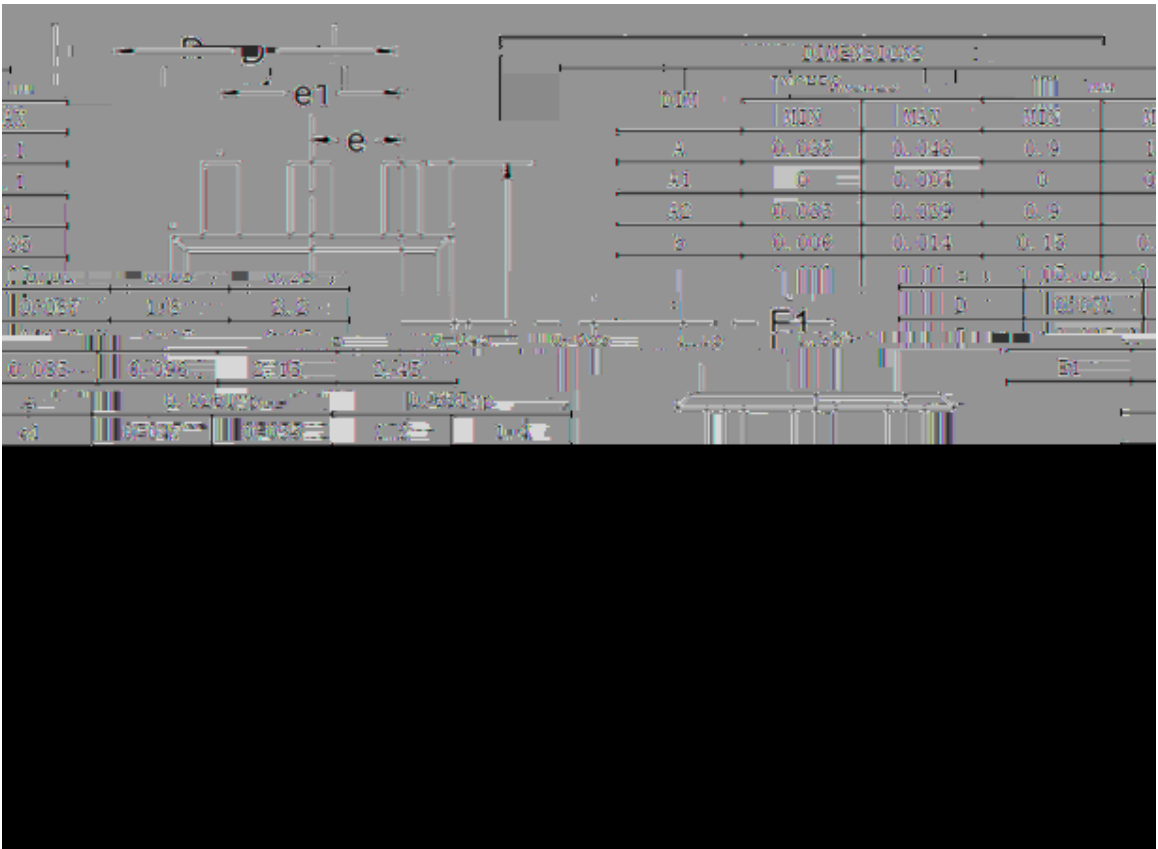


Characteristics (Typical)

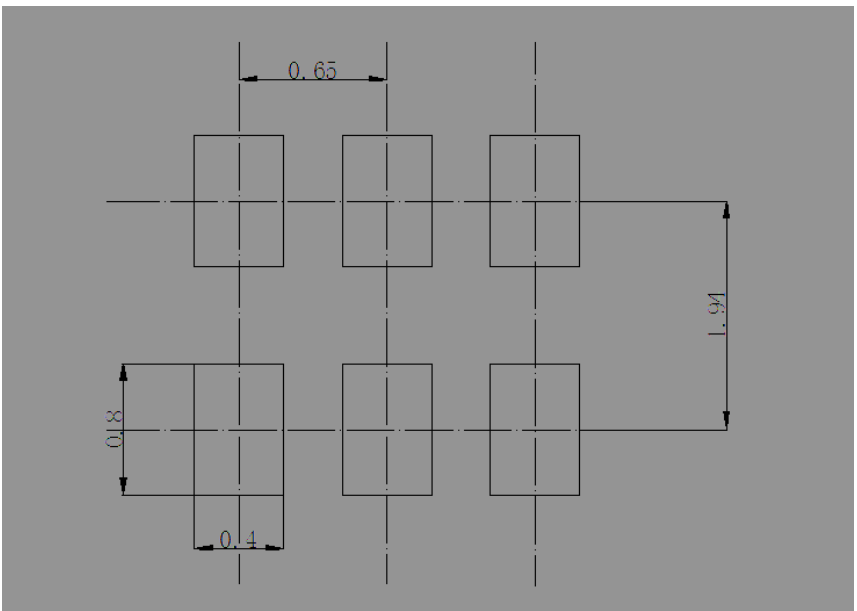




vSOT-363 Package Outline Dimensions



SOT-363 Suggested Pad Layout



Unit Ömm



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## Disclaimer

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